



BENEFITS FOR YOU



Certainty

Save resources and avoid re-spins.



Coverage

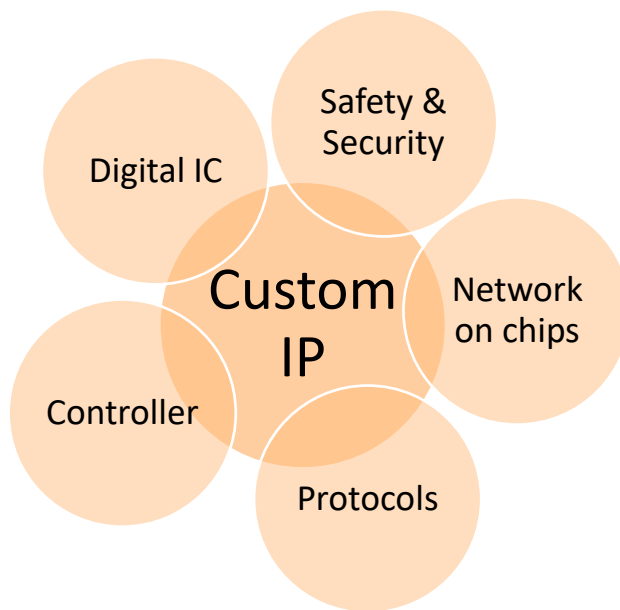
Reach your coverage goals in time.



Automation

Automatic generation of Verification IP.

TARGET AREAS



ABOUT US

LUBIS provides an innovative and smart approach for generating digital semiconductor IP. We are on the mission to reduce your time spend on verification and validation. We offer a solution that allows you to reach your coverage goals earlier, requires less RTL simulation and uncovers hard to find functional bugs in your design. Stay in your budget and avoid re-spins, with certainty. We are the company, that offers you trustable and sound system-level models, simulation environments and Verification IP.



Kaiserslautern,
Germany



8+



We love hardware

Let's start
a new project together

